Fig.1

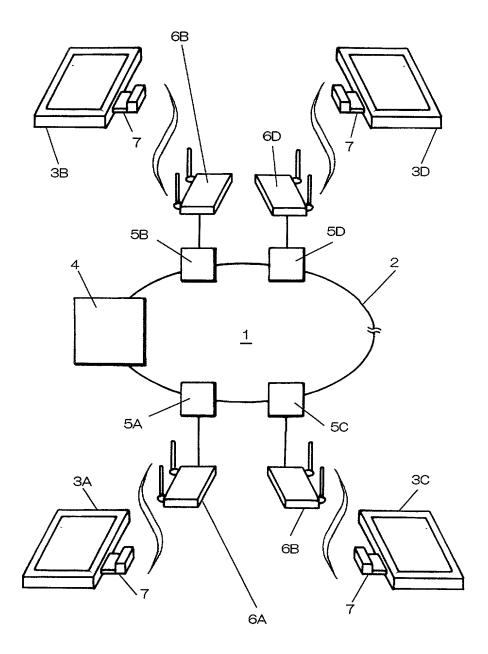
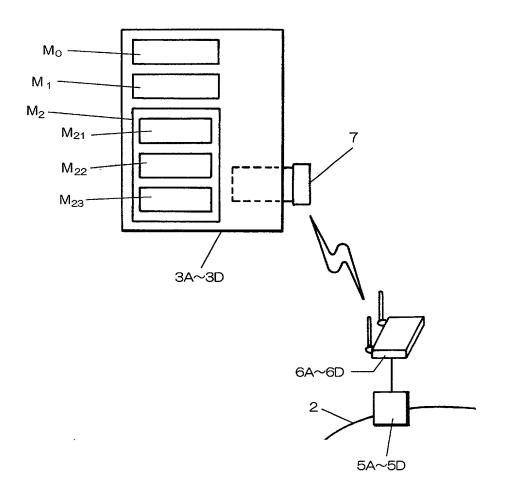
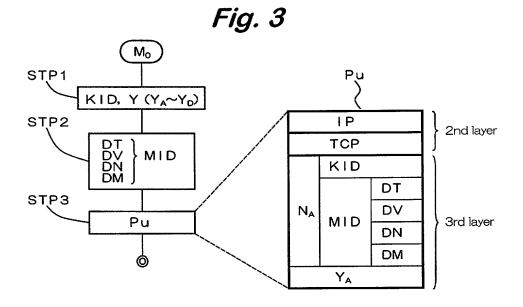


Fig.2





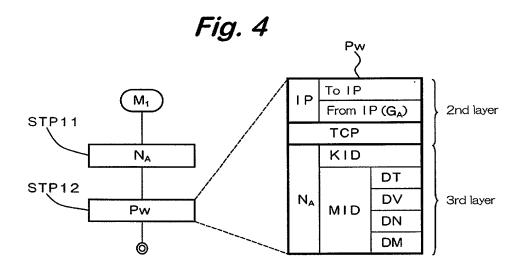
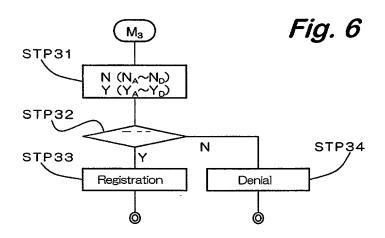


Fig. 5 M_2 STP₂₁ Ps Display To IP STP₂₂ IP 2nd layer From IP (GA) Input Y_B TCP STP23 KID Record STP24 DT NA N_A , Y_B MID DV 3rd layer STP₂₅ DN Sending DM YB 0



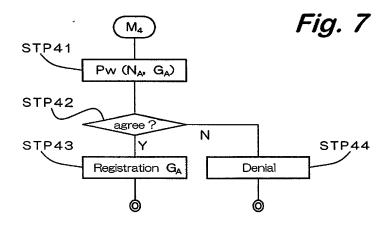


Fig. 8

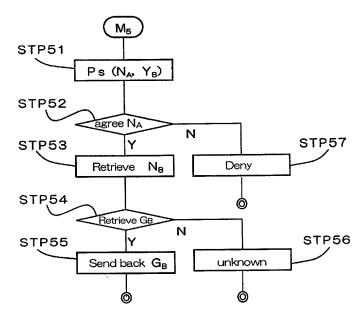


Fig. 9

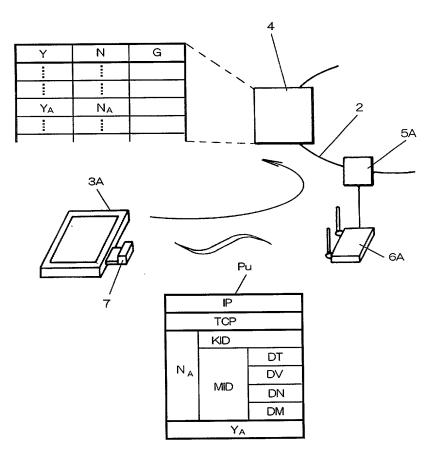
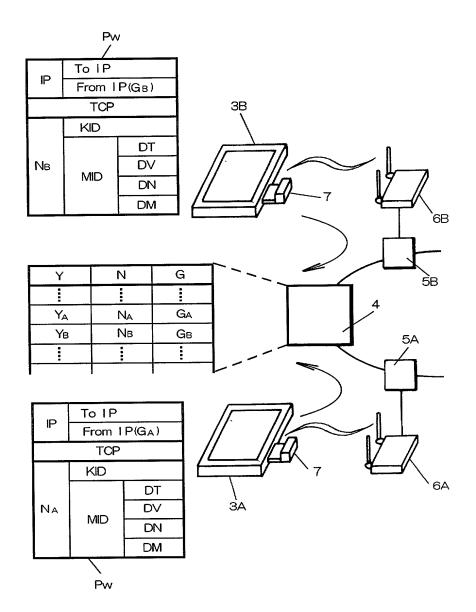
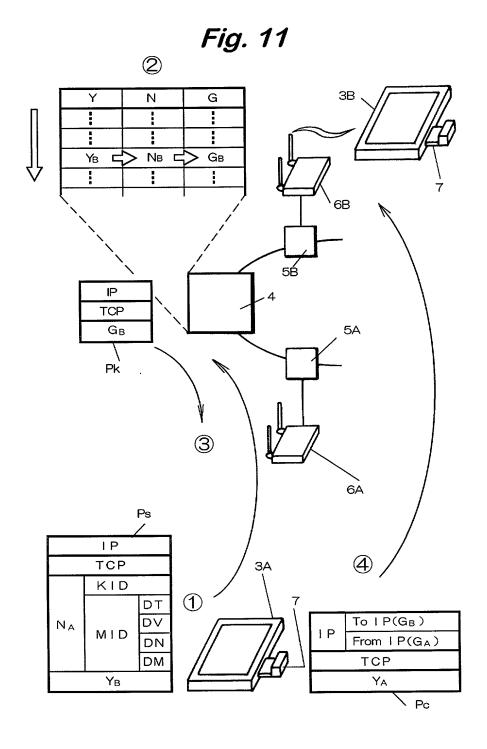


Fig. 10





- 1-400 T